

ABSTRACT OF THE DISCLOSURE

An image processing system comprising a combination of an image control unit and one or a plurality of image input/output units, wherein the image control unit and image input/output unit are connected by communication means. In a case where the image control unit has a first communication controller and the image input/output unit has a second communication controller, predetermined communication conditions are set in the first and second communication controllers at initialization, a fixed packet length is decided based upon mutually communicable packet lengths of packets exchanged via the communication means, the decided fixed packet length is set in the first and second communication controllers anew, and subsequent communication is continued by DMA transfer in which a data string is divided into the set fixed packet lengths. As a result, high-speed data communication is achieved between units even in a low-cost single-chip microcontroller.